



**FTDI
Chip**

Future Technology Devices Intl Limited

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Issue Date: 2008-01-30

PCN Tracking Number 00001

Product Change Notification

Product Part Number	FT232RQ, FT232RL, FT245RQ, FT245RL	Estimated implementation date
Description of Change	Metal layer change to wafer mask set	2008-01-30
Reason for Change	I/O voltage could not go below 2.6Volts. PWREN# pulsed during enumeration. RESET pulled up to VCCIO instead of VCC.	
Detailed Description	<ol style="list-style-type: none"> 1) Forced PWREN# and SLEEP# to input mode until configuration has been detected. These signals are NOT pulled up during this process and so will float. 2) VCCIO fixed to allow it to go below 2.5 volts 3) RESET is now pulled up to VCC instead of VCCIO 	
Impact to Data sheet	None. Should mention new part revision	
Benefit of Change	Allows customers to interface their peripherals at lower than 2.6 Volts. VCCIO can be taken to 0 volts and the chip will not enter reset condition. For power switched designs the fix to PWREN# will stop the power being turned on momentarily during enumeration.	
Markings to distinguish revised from:	-B is added after the date code	
Sample Availability	<input checked="" type="checkbox"/> YES, (<u>known schedule date</u>) <input type="checkbox"/> NO, (<u>estimate unknown schedule date</u>) NB these samples are "unbaked" so EEprom data retention is not tested	
Risk Assessment, Fit Form and Function & reliability	Should have minimal / no impact to most customers	
PDF Download	www.ftdichip.com/Documents/PCN.htm	

For more detail information, please contact sales1@ftdichip.com

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