



PCN Tracking Number: PCN_FT_005

Issue Date: 2010-04-15

Product Change Notification

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|---|--|--------------------------------------|
| Product Part Number | FT2232HQ, FT2232HL, FT4232HQ and FT4232HL | Estimated implementation date |
| Description of Change | Silicon modifications have resulted in the device being modified to Revision C | 15th April 2010 |
| Reason for Change | The Revision B silicon of the above devices marginally failed the maximum time to suspend in the USB compliance tests | |
| Detailed Description | <ol style="list-style-type: none">1. Suspend timer marginally failed USB chapter 9 test. Maximum time to suspend is 3.125ms. Rev B can be up to 4ms. This has been corrected.2. A test mode change to improve manufacturing test capability of the FT4232H.3. A test mode change to prevent the PHY entering test mode on a packaged device.4. The wait on IO1 signal in CPU emulation mode has been corrected. | |
| Impact to Data sheet | There is no impact to the datasheet. | |
| Benefit of Change | To meet USB compliance testing. Improved testability. Corrected wait operation in CPU emulation mode. | |
| Markings to distinguish revised from: | Markings documents will be updated to reflect that the devices listed will now be marked as Rev C. | |
| Sample Availability | April 2010 | |
| Risk Assessment, Fit Form and Function & reliability | No risk to existing functionality. | |
| PDF Download | http://www.ftdichip.com/branding/PCN_FT_005.pdf | |

Others:

There are no functional differences between Rev B and Rev C with the exception of the suspend timer and correction to the IO1 signal in CPU emulation mode. Existing applications will not be affected and will require no modifications.

Mass production availability : May 2010