This application note forms part of a series of application notes detailing the new simplified ROM images for VNC2. It will detail the implementation and use of a VNC2 ROM file for bridging a SPI slave interface to hosting a USB memory device.

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1 Introduction

FTDI have introduced a new suite of simplified “bridging” ROM files to allow for fast interconnect between differing interfaces. These ROM images (and sample code) are targeted at those users who would like to implement VNC2 into a design without creating their own firmware.

As well as providing the source code for users wishing to tweak setup parameters the default project will be supplied as a precompiled ROM file ready for installation into the VNC2.

This application note forms part of a series of application notes detailing the new simplified ROM images for VNC2. This document will detail the implementation and use of a VNC2 ROM file for bridging a SPI interface to hosting a Bulk Only Mass storage (BOMS) class device on a USB port (USB memory). This particular project may be used in 32, 48 or 64 pin packages.

For users not intending to edit the code in any way the precompiled code may be loaded over the UART interface with FT_PROG as an alternative to using the IDE. Links for the project file SPI2DSC.vproj and the precompiled ROM file SPI2DSC.rom file may be found at the end of the document in Appendix A.

1.1 VNC2 Devices

VNC2 is the second of FTDI’s Vinculum family of embedded dual USB host controller devices. The VNC2 device provides USB Host interfacing capability for a variety of different USB device classes including support for BOMS (bulk only mass storage), Printer and HID (human interface devices). For mass storage devices such as USB Flash drives, VNC2 transparently handles the FAT file structure.

Communication with non USB devices, such as a low cost microcontroller, is accomplished via either UART, SPI or parallel FIFO interfaces. VNC2 provides a new, cost effective solution for providing USB Host capability into products that previously did not have the hardware resources available.

VNC2 allows customers to develop their own firmware using the Vinculum II software development tool suite. These development tools provide compiler, assembler, linker and debugger tools complete within an integrated development environment (IDE).

The Vinculum-II VNC2 family of devices are available in Pb-free (RoHS compliant) 32-lead LQFP, 32-lead QFN, 48-lead LQFP, 48-lead QFN, 64-Lead LQFP and 64-lead QFN packages For more information on the ICs refer to http://www.ftdichip.com/Products/ICs/VNC2.htm.
2 Using the Sample Code

![Diagram of SPI connection](image)

**Figure 2.1 Connecting the Demo**

When the VNC2 is programmed, the user simply passes data from the SPI master to the VNC2. This data will be appended to a file named "TEST.TXT".

The default configuration of the SPI slave port on the VNC2 is:

- Clock phase, CPHA = 0
- Clock polarity, CPOL = 0
- Data order is MSB first
- Address = 0
- VNC2 SPI mode = unmanaged

There are no additional commands required; it is a simple data bridge.

### 2.1 LEDs

Although GPIO lines have been reserved for LED driving the pins were not coded to signal anything.
## 3 Default Pin-Out

<table>
<thead>
<tr>
<th>Signal</th>
<th>32-pin pkg</th>
<th>48-pin pkg</th>
<th>64-pin pkg</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>GND</td>
<td>1, 16, 19, 27</td>
<td>1, 24, 27, 39</td>
<td>1, 30, 35, 53</td>
<td>Device ground supply pins</td>
</tr>
<tr>
<td>3V3 VREGIN</td>
<td>2</td>
<td>2</td>
<td>2</td>
<td>+3.3VDC supply to the regulator</td>
</tr>
<tr>
<td>1V8 VCC PLL IN</td>
<td>3</td>
<td>3</td>
<td>3</td>
<td>+1.8VDC supply to internal clock multiplier</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Requires 100nF decoupling capacitor close to pin</td>
</tr>
<tr>
<td>GND PLL</td>
<td>6</td>
<td>6</td>
<td>6</td>
<td>Device analog ground supply for internal clock multiplier</td>
</tr>
<tr>
<td>VREG OUT</td>
<td>7</td>
<td>7*</td>
<td>7</td>
<td>+1.8VDC output from regulator to device core</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>*Not used on 48-pin LQFP package</td>
</tr>
<tr>
<td>VCCIO</td>
<td>13, 22, 28</td>
<td>17, 30, 40</td>
<td>21, 38, 54</td>
<td>+3.3VDC supply to I/O interface pins (IOBUS)</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>VCCIO must be connected for proper operation</td>
</tr>
<tr>
<td>XTIN</td>
<td>4</td>
<td>4</td>
<td>4</td>
<td>Input to 12MHz Oscillator</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Connect 12MHz crystal across pins 4 and 5 with proper loading capacitance</td>
</tr>
<tr>
<td>XTOUT</td>
<td>5</td>
<td>5</td>
<td>5</td>
<td>Output from 12MHz Oscillator</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Connect 12MHz crystal across pins 4 and 5 with proper loading capacitance</td>
</tr>
<tr>
<td>TEST</td>
<td>8</td>
<td>8</td>
<td>8</td>
<td>Test – Must be connected to GND for normal operation</td>
</tr>
<tr>
<td>RESET#</td>
<td>10</td>
<td>9</td>
<td>9</td>
<td>Can be used by an external device to reset VNC2</td>
</tr>
<tr>
<td>PROG#</td>
<td>9</td>
<td>10</td>
<td>10</td>
<td>Asserting PROG# enables program mode</td>
</tr>
<tr>
<td>DEBUGGER I/F</td>
<td>11</td>
<td>11</td>
<td>11</td>
<td>I/O for programming and in-circuit debugging</td>
</tr>
<tr>
<td>USB DP1</td>
<td>17</td>
<td>25</td>
<td>33</td>
<td>USB Port 1 Data Plus</td>
</tr>
<tr>
<td>USB DM1</td>
<td>18</td>
<td>26</td>
<td>34</td>
<td>USB Port 1 Data Minus</td>
</tr>
<tr>
<td>SPI_S0_CLK</td>
<td>29</td>
<td>15</td>
<td>61</td>
<td>SPI Slave Clock input</td>
</tr>
<tr>
<td>SPI_S0_MOSI</td>
<td>30</td>
<td>16</td>
<td>62</td>
<td>SPI MOSI input</td>
</tr>
<tr>
<td>Signal</td>
<td>32-pin pkg</td>
<td>48-pin pkg</td>
<td>64-pin pkg</td>
<td>Description</td>
</tr>
<tr>
<td>------------</td>
<td>------------</td>
<td>------------</td>
<td>------------</td>
<td>-------------------------</td>
</tr>
<tr>
<td>SPI_S0_MISO</td>
<td>31</td>
<td>18</td>
<td>63</td>
<td>SPI MISO output</td>
</tr>
<tr>
<td>SPI_S0_SS#</td>
<td>32</td>
<td>19</td>
<td>64</td>
<td>SPI Slave select input</td>
</tr>
</tbody>
</table>

Table 3.1 VNC2 Pin-Out
4 Building and Loading the Firmware in the VNC2

Everything can be controlled by the IDE. To access the application simply use the Project -> Open tab to browse to the SPI2DSC.vproj file for your project.

4.1 Build

This step is only necessary if you are not using the precompiled version of the ROM. Otherwise proceed to loading the ROM file.

To build the application you simply press the Build button on the IDE ribbon bar under the build tab.

4.2 Load

Loading the code is equally simple. Just click on the "Flash" button on the ribbon bar under the debug tab. The Flash button will automatically pick up the Rom file in your project but for reference the filename is SPI2DSC.ROM.

Note the Debugger Interface is listed as V2EVAL Board C. It is important that this box shows a device is connected before attempting to flash a device.
4.3 Loading with FT_Prog

If the precompiled ROM file meets all the requirements of the end design then the IDE and source code are not required. The precompiled FT232Uart.ROM file may be loaded into the VNC2 with FT_PROG over the UART interface.

FT_Prog is used to program the VNC2 with a ROM file. FT_Prog is available from the FTDI website utilities page (version 1.12 or later supports VNC2).

- Select the flash ROM tab at the top of the window.
- Select VNC2 from the pull down tab.
- Select D2xx or VCP interface (either will work).
- Select the location where the ROM file resides.
- Press the program button.
- Perform a hard reset (power cycle) prior to running the firmware.

Figure 4.4 is an example of programming the VNC2 Evaluation board revision 2 with the V2DAP firmware.

Figure 4.4 FT_Prog Programming Utility

For more information on loading ROM files onto a VNC2 device refer to:
5 Source Code for the VNC2 Application

This section is aimed at those wanting to learn about coding VNC2 devices or modify the existing project.

All VNC2 application firmware follows a similar format and most of the code can be “written” using the IDE application wizard.

The basic steps are:

1. Initialise device drivers
2. Define pin-outs
3. Open ports to be used
4. Configure ports to be used
5. Read/write data
6. Close ports

The VNC2 source code for this project can be viewed in appendix A and is available to download with the toolchain.

5.1 SPI2DSC.C

SPI2DSC.c is the main firmware file. This file is split into multiple functions.

```c
void main();
void iomux_setup(void);
unsigned char usbhost_connect_state(VOS_HANDLE hUSB)
VOS_HANDLE fat_attach(VOS_HANDLE hMSI, unsigned char devFAT)
void fat_detach();
VOS_HANDLE boms_attach(VOS_HANDLE hUSB, unsigned char devBOMS)
void boms_detach();
void open_drivers(void);
void monError(void);
void setup();
void SPIISlave(void)
void BOMS(void)
```

5.1.1 main()

Main is where the application starts. It defines the VNC2 core clock speed, loads the drivers to be used and creates the threads to be used in the application. At the very end of main is the call vos_start_scheduler();

After this call there can be no further configuration of the device.

5.1.2 iomux_setup()

Iomux_setup actually refers to the other file in the project, SPI2DSC_iomux.c and is used to define the VNC2 pin-out. Most functions can be programmed to appear on different pins. The notable exceptions are power, GND and the USB ports.

5.1.3 USB_Host_Connect_state

USB_Host_Connect_State is a function to check if anything is connected to the USB host.
5.1.4 fat_attach/ Fat_detach
This section will attach the FAT driver to the BOMS class driver. This is the last layer of the driver construction to connect the USB port to a FAT table in a memory device. Fat_detach disconnects the link.

5.1.5 Boms_attach/ boms_detach
This section will attach the BOMS class driver to the USB host port 1 driver.
Boms_detach disconnects the link.

5.1.6 Open drivers
The open drivers function call will provide a handle to each hardware block used in the project and this handle can be used by subsequent commands to control the hardware.

5.1.7 monError
This function will light an LED if an error is detected.

5.1.8 Setup
Setup() will ensure the driver handles are opened, the USB driver is connected to the BOMs driver, which is then connected to the FAT driver. It will also configure the SPI slave 0 port.

5.1.9 SPI Slave
This section will allow the VNC2 to read data sent to it over SPI.

5.1.10 BOMS
This section will transfer the data read on the SPI interface to a file named “TEST.txt” on the memory device connected to the USB port of the VNC2.
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Appendix A – References

Document References
Application and Technical Notes available at
VNC2 Datasheet
V2-EVAL datasheet
Vinculum II Toolchain
AN_139 IO_Mux explained
AN_151 Vinculum II User Guide
AN_159_Vinculum_II_Firmware_Flash_Programming.pdf

Project source code download
http://www.ftdichip.com/Firmware/Precompiled/SPI2DSC.zip

Project precompiled ROM file download
http://www.ftdichip.com/Firmware/Precompiled/SPI2DSC.ROM

Acronyms and Abbreviations

<table>
<thead>
<tr>
<th>Terms</th>
<th>Description</th>
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<tbody>
<tr>
<td>SPI</td>
<td>Serial Peripheral Interface</td>
</tr>
<tr>
<td>USB</td>
<td>Universal Serial Bus</td>
</tr>
<tr>
<td>USB-IF</td>
<td>USB Implementers Forum</td>
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## Appendix C – Revision History

Document Title: AN_186 Vinculum-II SPI to USB Memory Bridge  
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Clearance No.: FTDI# 233  
Product Page: [http://www.ftdichip.com/Products/ICs/VNC2.htm](http://www.ftdichip.com/Products/ICs/VNC2.htm)  
Document Feedback: [Send Feedback](#)

<table>
<thead>
<tr>
<th>Revision</th>
<th>Changes</th>
<th>Date</th>
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<tr>
<td>1.0</td>
<td>Initial Release</td>
<td>2011-11-14</td>
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