# Introduction

The UMFT234XD breakout module utilizes FTDI’s FT234XD IC to convert USB to UART.

## 1.1 Features

The UMFT234XD is a breakout board that converts USB2.0 Full-Speed to UART. These modules do not have a USB connector, instead, the modules plug directly into the USB host connector and the pads of the PCB makes electrical contact with the electrical contacts of the USB connector.

The UART interface operates at +3.3V voltage levels, however all I/Os are 5V tolerant.

## 1.2 Ordering Information

<table>
<thead>
<tr>
<th>Module</th>
<th>Interface</th>
<th>Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>UMFT234XD-01</td>
<td>UART</td>
<td>USB to UART with four control bus lines, 3.3V power out limited to 50mA and 5V with as safety fuse. UART signals available via 8 pin female connector.</td>
</tr>
<tr>
<td>UMFT234XD-NC</td>
<td>UART</td>
<td>USB to UART with four control bus lines, 3.3V power out limited to 50mA and 5V with as safety fuse. UART signals available via 8 pads on the PCB.</td>
</tr>
<tr>
<td>UMFT234XD-WE</td>
<td>UART</td>
<td>USB to UART with four control bus lines, 3.3V power out limited to 50mA and 5V with as safety fuse. UART signals available via 8, 6&quot; flying leads connected to the PCB pads.</td>
</tr>
</tbody>
</table>

## 2 Driver Support

### Royalty-Free VIRTUAL COM PORT (VCP):

- Windows 8 32,64-bit
- Windows 7 32,64-bit
- Windows Vista
- Windows XP 32,64-bit
- Windows XP Embedded
- Windows CE.NET 4.2, 5.0 and 6.0
- MAC OS OS-X
- Linux 3.0 and greater
- Android

### Royalty-Free D2XX Direct Drivers:

- Windows 8 32,64-bit
- Windows 7 32,64-bit
- Windows Vista
- Windows XP 32,64-bit
- Windows XP Embedded
- Windows CE.NET 4.2, 5.0 and 6.0
- MAC OS OS-X
- Linux 3.0 and greater
- Android

The drivers listed above are all available to download for free from [www.ftdichip.com](http://www.ftdichip.com). Various 3rd Party Drivers are also available for various other operating systems - visit [www.ftdichip.com](http://www.ftdichip.com) for details.

Use of FTDI devices in life support and/or safety applications is entirely at the user’s risk, and the user agrees to defend, indemnify and hold harmless FTDI from any and all damages, claims, suits or expense resulting from such use.
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3 UMFT234XD Signals and Configurations

CN1 connects directly to a USB host or HUB port, or can be connected to a USB extension cable. This connects to USB signals, 5V USB Bus power and GND. When connecting the module to a USB host or HUB the signal pads should be facing upwards when connecting to a horizontal connector and be facing right for vertical connectors. If the module is plugged in upside down no connectivity will be made between PCB and HUB, no damage will occur from plugging the module in the wrong way.

3.1 UMFT234XD CN1 Signal Descriptions

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>VBUS</td>
<td>PWR</td>
<td>5V Power input USB port. For a low power USB bus powered design, up to 100mA can be sourced from the 5V supply on the USB bus. A maximum of 500mA can be sourced from the USB bus in a high power USB bus powered design.</td>
</tr>
<tr>
<td>2</td>
<td>D-</td>
<td>Signal</td>
<td>Negative USB data signal</td>
</tr>
<tr>
<td>3</td>
<td>D+</td>
<td>Signal</td>
<td>Positive USB data signal</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
<td>PWR</td>
<td>Module Ground</td>
</tr>
</tbody>
</table>

Table 3.1 USB Connector Pin Out Description

3.2 UMFT234XD CN2 Signal Descriptions

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>TXD</td>
<td>Output</td>
<td>UART transmit data</td>
</tr>
<tr>
<td>2</td>
<td>VBUS</td>
<td>PWR</td>
<td>5V Power output USB port.</td>
</tr>
<tr>
<td>3</td>
<td>GND</td>
<td>PWR</td>
<td>Module Ground Supply</td>
</tr>
<tr>
<td>4</td>
<td>RESET#</td>
<td>Input</td>
<td>Can be used by an external device to reset the FT234XD.</td>
</tr>
<tr>
<td>5</td>
<td>RXD</td>
<td>Input</td>
<td>UART receive data</td>
</tr>
<tr>
<td>6</td>
<td>3V3</td>
<td>Output</td>
<td>3.3V output from FT234XD integrated LDO regulator. This pin is decoupled to ground on the module PCB with a 10nF capacitor and offers 3V3 at up to 50mA for external hardware.</td>
</tr>
<tr>
<td>7</td>
<td>RTS#</td>
<td>Output</td>
<td>UART Ready to Send handshake signal</td>
</tr>
<tr>
<td>8</td>
<td>GND</td>
<td>PWR</td>
<td>Module Ground Supply</td>
</tr>
<tr>
<td>9</td>
<td>CTS#</td>
<td>Input</td>
<td>UART Clear to Send handshake signal</td>
</tr>
<tr>
<td>10</td>
<td>CB0</td>
<td>I/O</td>
<td>Configurable CBUS0 I/O Pin. The function of this pin is configured in the device internal MTP ROM. See Table 3.3</td>
</tr>
</tbody>
</table>

Table 3.2 UART Module Pin Out Description
## 3.3 CBUS Signal Options

<table>
<thead>
<tr>
<th>CBUS Signal Option</th>
<th>Available On CBUS Pin</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tristate</td>
<td>CBUS0</td>
<td>IO Pad is tri-stated</td>
</tr>
<tr>
<td>DRIVE_1</td>
<td>CBUS0</td>
<td>Output a constant 1</td>
</tr>
<tr>
<td>DRIVE_0</td>
<td>CBUS0</td>
<td>Output a constant 0</td>
</tr>
<tr>
<td>TXDEN</td>
<td>CBUS0</td>
<td>Enable transmit data for RS485</td>
</tr>
<tr>
<td>PWREN#</td>
<td>CBUS0</td>
<td>Output is low after the device has been configured by USB, then high during USB suspend mode. This output can be used to control power to external logic.</td>
</tr>
<tr>
<td>TXLED#</td>
<td>CBUS0</td>
<td>Transmit data LED drive – open drain pulses low when transmitting data via UART.</td>
</tr>
<tr>
<td>RXLED#</td>
<td>CBUS0</td>
<td>Receive data LED drive – open drain pulses low when receiving data via UART.</td>
</tr>
<tr>
<td>TX&amp;RXLED#</td>
<td>CBUS0</td>
<td>LED drive – open drain pulses low when transmitting or receiving data via UART.</td>
</tr>
<tr>
<td>SLEEP#</td>
<td>CBUS0</td>
<td>Goes low during USB suspend mode. Typically used to power down external logic to RS232 level converter IC in USB to RS232 converter designs</td>
</tr>
<tr>
<td>CLK24MHz</td>
<td>CBUS0</td>
<td>24 MHz Clock output.**</td>
</tr>
<tr>
<td>CLK12MHz</td>
<td>CBUS0</td>
<td>12 MHz Clock output.**</td>
</tr>
<tr>
<td>CLK6MHz</td>
<td>CBUS0</td>
<td>6 MHz Clock output.**</td>
</tr>
<tr>
<td>GPIO</td>
<td>CBUS0</td>
<td>CBUS bit bang mode option. Allows up to 4 of the CBUS pins to be used as general purpose I/O. A separate application note, AN232R-01, available from FTDI website (<a href="http://www.ftdichip.com">www.ftdichip.com</a>) describes in more detail how to use CBUS bit bang mode.</td>
</tr>
<tr>
<td>BCD_Charger</td>
<td>CBUS0</td>
<td>Battery Charge Detect indicates when the device is connected to a dedicated battery charger host. Active high output. NOTE: Requires a 10K pull-down to remove power up toggling.</td>
</tr>
<tr>
<td>BCD_Charger#</td>
<td>CBUS0</td>
<td>Active low BCD Charger, driven by an open drain to ground with no internal pull-up.</td>
</tr>
<tr>
<td>BitBang_WR#</td>
<td>CBUS0</td>
<td>Synchronous and asynchronous bit bang mode WR# strobe output.</td>
</tr>
<tr>
<td>BitBang_RD#</td>
<td>CBUS0</td>
<td>Synchronous and asynchronous bit bang mode RD# strobe output.</td>
</tr>
<tr>
<td>VBUS_Sense</td>
<td>CBUS0</td>
<td>Input to detect when VBUS is present.</td>
</tr>
<tr>
<td>Time_Stamp</td>
<td>CBUS0</td>
<td>Toggle signal which changes state each time a USB SOF is received</td>
</tr>
<tr>
<td>Keep_Awake#</td>
<td>CBUS0</td>
<td>Active Low input, prevents the chip from going into suspend.</td>
</tr>
</tbody>
</table>

### Table 3.3 CBUS Configuration Control

* PWREN# must be used with a 10kΩ resistor pull up.
**When in USB suspend mode the outputs clocks are also suspended.
3.4 Configuring the MTP ROM

The FT234XD IC on the module contains an embedded MTP ROM, this can be used to specify the functions of the CBUS0 pin, the current drive on each signal pin, current limit for the USB bus and the descriptors of the device. These features can be programmed using FTDI’s programming utility FT_Prog. For details on using FT_Prog, please see the FTDI Prog User Guide.

When programming the MTP ROM please note:

i) The Max Bus Power setting of the MTP ROM should specify the maximum current to be drawn from the USB host/hub when enumerated. For high-powered USB devices the current limit when enumerated is between 100mA and 500mA, for low-powered USB devices the current limit is 100mA.

4 Module Dimensions

![Module Dimensions Diagram]

Measurements given in millimetres
Tolerance +/-0.1mm

Figure 4.1 UMFT234XD-01 Module Dimensions
5 Module Wire Connections

5.1 UMFT234XD-WE Wire Connections

Figure 5.1 UMFT234XD-WE Wire Connections (numbers refer to pad numbers on the PCB)

Figure 5.1 illustrates the –WE product as a cable. This is only for illustration purposes. The wire ended product consists of individual wires – not a cable

5.2 UMFT234XD-WE

Figure 5.2 UMFT234XD-WE Image
6 Module Circuit Schematic

6.1 UMFT234XD Schematic

![UMFT234XD Circuit Schematic](image)

Figure 6.1 UMFT234XD Circuit Schematic

7 Environmental Compliances

The UMFT234XD modules exclusively use lead free components, and are fully compliant with European Union directive 2002/95/EC.
8 Internal MTP ROM Configuration

Following a power-on reset or a USB reset the FT234XD will scan its internal MTP ROM and read the USB configuration descriptors stored there. The default values programmed into the internal MTP ROM in the FT234XD used on the UMFT234XD are shown in Table 8.1.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>USB Vendor ID (VID)</td>
<td>0403h</td>
<td>FTDI default VID (hex)</td>
</tr>
<tr>
<td>USB Product UD (PID)</td>
<td>6015h</td>
<td>FTDI default PID (hex)</td>
</tr>
<tr>
<td>Serial Number Enabled?</td>
<td>Yes</td>
<td></td>
</tr>
<tr>
<td>Serial Number</td>
<td>See Note</td>
<td></td>
</tr>
<tr>
<td>Pull down I/O Pins in USB Suspend</td>
<td>Disabled</td>
<td>Enabling this option will make the device pull down on the UART interface lines when the power is shut off (PWREN# is high).</td>
</tr>
<tr>
<td>Manufacturer Name</td>
<td>FTDI</td>
<td></td>
</tr>
<tr>
<td>Product Description</td>
<td>UMFT234XD</td>
<td></td>
</tr>
<tr>
<td>Max Bus Power Current</td>
<td>90mA</td>
<td></td>
</tr>
<tr>
<td>Power Source</td>
<td>Bus Powered</td>
<td></td>
</tr>
<tr>
<td>Device Type</td>
<td>FT234XD</td>
<td></td>
</tr>
<tr>
<td>USB Version</td>
<td>0200</td>
<td>Returns USB 2.0 device description to the host. Note: The device is a USB 2.0 Full Speed device (12Mb/s).</td>
</tr>
<tr>
<td>Remote Wake Up</td>
<td>Enabled</td>
<td>Taking RI# low will wake up the USB host controller from suspend.</td>
</tr>
<tr>
<td>High Current I/Os</td>
<td>Disabled</td>
<td>Enables the high drive level on the serial and CBUS I/O pins.</td>
</tr>
<tr>
<td>Load VCP Driver</td>
<td>Enabled</td>
<td>Makes the device load the VCP driver interface for the device.</td>
</tr>
<tr>
<td>CBUS0</td>
<td>PWREN#</td>
<td></td>
</tr>
</tbody>
</table>

Table 8.1 Default Internal MTP ROM Configuration

The internal MTP ROM in the FT234XD can be programmed over USB using the utility program FT_PROG. FT_PROG can be downloaded from www.ftdichip.com. Users who do not have their own USB vendor ID but who would like to use a unique Product ID in their design can apply to FTDI for a free block of unique PIDs. Contact FTDI Support (support1@ftdichip.com) for this service.
9 Contact Information

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**Web Site**

http://ftdichip.com

**Distributor and Sales Representatives**

Please visit the Sales Network page of the FTDI Web site for the contact details of our distributor(s) and sales representative(s) in your country.

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# Appendix B: Revision History

<table>
<thead>
<tr>
<th>Version</th>
<th>Description</th>
<th>Date</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0</td>
<td>Initial Datasheet Created</td>
<td>31/01/13</td>
</tr>
<tr>
<td>1.1</td>
<td>Corrected -01 part number</td>
<td>06/07/15</td>
</tr>
</tbody>
</table>

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Document Reference No.: FT_000801  
Clearance No.: FTDI# 327  
Document Feedback: [Send Feedback](#)