The intention of this technical note is to give a brief overview of the construction and format of a USB message packet.
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1 Introduction

Many devices on the market today connect via the USB interface. This technical note will attempt to explain the protocol used for data transfers between USB hosts and USB devices.
2 USB Speed

There are 3 speed USB grades currently on the market:

- Slow speed – 1.5Mbps
- Full speed – 12Mbps
- High speed – 480mbps

A 4th class specification – super speed at 4.8Gbps has now been ratified, which should result in even faster devices coming to the market soon.
3 USB DATA FORMAT

USB data is sent in packets Least Significant Bit (LSB) first.

There are 4 main USB packet types: Token, Data, Handshake and Start of Frame.

Each packet is constructed from different field types, namely SYNC, PID, Address, Data, Endpoint, CRC and EOP.

The packets are then bundled into frames to create a USB message.
4 USB Data States

The USB data is transferred on a differential serial line (USB DP and USB DM), using NRZI coding.

Many documents refer to the J and K states on the USB data lines. These are used for USB packet synchronisation and defining the end of USB packets.

A J state has a differential signal on USB DP and USB DM >= +300mV.

A K state has a differential signal on USB DP and USB DM >= -300mV.

A Single Ended Zero (SE0) is where both USB DM and USB DM are at 0V.
5 Fields

Fields are the building blocks of a USB packet. These describe the data at individual bit level.

5.1 Sync Field

Each USB packet starts with a SYNC field. This is basically used to synchronise the transmitter and the receiver so that the data can be transferred accurately.

In a USB slow / full speed system this SYNC field consists 3 KJ pairs followed by 2 K’s to make up 8 bits of data.

In a USB Hi-Speed system the synchronisation requires 15 KJ pairs followed by 2 K’s to make up 32 bits of data.

5.2 Packet Identifier Field

Following on directly after the SYNC field is the Packet Identifier Field. The Packet Identifier Field consists of a 4 bit identifier and a further 4 bits which are the one’s compliment of the identifier.

<table>
<thead>
<tr>
<th>PID0</th>
<th>PID1</th>
<th>PID2</th>
<th>PID3</th>
<th>/PID0</th>
<th>/PID1</th>
<th>/PID2</th>
<th>/PID3</th>
</tr>
</thead>
</table>

Figure 5.2: PID Field.

5.3 Address Field

The address field is 7 bits long. It is used with the endpoint field to address a function. Address 0 is a special case reserved for access immediately after power up and reset.

<table>
<thead>
<tr>
<th>ADDR0</th>
<th>ADDR1</th>
<th>ADDR2</th>
<th>ADDR3</th>
<th>ADDR4</th>
<th>ADDR5</th>
<th>ADDR6</th>
</tr>
</thead>
</table>

Figure 5.3: Address Field.

5.4 Endpoint Field

The endpoint field is 4 bits long and allows for additional flexibility in addressing. Endpoints are usually split for data going IN or OUT. Endpoint 0 is a special case referred to as the CONTROL endpoint and every device has an endpoint 0.

|-----|-----|-----|-----|

Figure 5.4: Endpoint Field.

5.5 Data Field

The data field is not a fixed length. It is within the range of 0 - 8192 bits long, and always an integral number of bytes.
5.6 CRC Field

There are two CRC fields.

The CRC5 is 5 bits long and used with the token packet and the start of frame packet.

<table>
<thead>
<tr>
<th>CRC0</th>
<th>CRC1</th>
<th>CRC2</th>
<th>CRC3</th>
<th>CRC4</th>
</tr>
</thead>
</table>

Figure 5.6.1: CRC5 Field

The CRC16 is 16 bits long and used with the data packet.

<table>
<thead>
<tr>
<th>CRC0</th>
<th>CRC1</th>
<th>CRC2</th>
<th>CRC3</th>
<th>CRC4</th>
<th>CRC5</th>
<th>CRC6</th>
<th>CRC7</th>
<th>CRC8</th>
<th>CRC9</th>
<th>CRC10</th>
<th>CRC11</th>
<th>CRC12</th>
<th>CRC13</th>
<th>CRC14</th>
<th>CRC15</th>
</tr>
</thead>
</table>

Figure 5.6.2: CRC16 Field

These fields are used for cyclic redundancy checking, that is they are used for error detection.

5.7 EOP Field

Each packet is terminated with an End of Packet (EOP) field. This consists of a single ended zero (SE0) for two bit times followed by a J for 1 bit time.
6 USB Packets

6.1 USB TOKEN Packets

The USB token packet is used to access the correct address and endpoint. It is constructed with the SYNC, PID, an 8 bit PID field, followed by a 7 bit address, followed by a 4 bit endpoint and a 5 bit CRC.

<table>
<thead>
<tr>
<th>FIELD</th>
<th>SYNC</th>
<th>PID</th>
<th>ADDRESS</th>
<th>ENDPOINT</th>
<th>CRC5</th>
<th>EOP</th>
</tr>
</thead>
<tbody>
<tr>
<td>#BITS</td>
<td>8/32</td>
<td>8</td>
<td>7</td>
<td>4</td>
<td>5</td>
<td>3</td>
</tr>
</tbody>
</table>

Figure 6.1: TOKEN Packet.

Both the address and endpoint field must be correctly decoded for correct operation.

6.2 USB DATA Packets

The data packet may be of variable length, dependent upon the data. However, the data field will be an integral number of bytes.

<table>
<thead>
<tr>
<th>FIELD</th>
<th>SYNC</th>
<th>PID</th>
<th>DATA</th>
<th>CRC16</th>
<th>EOP</th>
</tr>
</thead>
<tbody>
<tr>
<td>#BITS</td>
<td>8/32</td>
<td>8</td>
<td>0-8192</td>
<td>16</td>
<td>3</td>
</tr>
</tbody>
</table>

Figure 6.2: DATA Packet.

6.3 USB Handshake Packets

Handshake packets are used to signal the status of a transaction ie pass/fail.

<table>
<thead>
<tr>
<th>FIELD</th>
<th>SYNC</th>
<th>PID</th>
<th>EOP</th>
</tr>
</thead>
<tbody>
<tr>
<td>#BITS</td>
<td>8/32</td>
<td>8</td>
<td>3</td>
</tr>
</tbody>
</table>

Figure 6.3: Handshake Packet.

6.4 USB Start of Frame Packets

To complete a full message the packets are grouped into frames. To identify the frame you need a Start of Frame packet (SOP). The frame number is 11 bits long.

<table>
<thead>
<tr>
<th>FIELD</th>
<th>SYNC</th>
<th>PID</th>
<th>Frame Number</th>
<th>CRC5</th>
<th>EOP</th>
</tr>
</thead>
<tbody>
<tr>
<td>#BITS</td>
<td>8/32</td>
<td>8</td>
<td>11</td>
<td>5</td>
<td>3</td>
</tr>
</tbody>
</table>

Figure 6.4: Start of Frame Packet.

The start of frame packet is sent every 1ms on a USB full speed system to help synchronisation.

On a USB Hi-Speed speed system a packet frame is transferred every 125us, resulting in 8 packets every 1ms. These 8 sub frames all contain the same frame number.
7 How FTDI Help

The construction of a USB message is not trivial as can be seen from the previous chapters. FTDI silicon handles this packet construction and deals with the USB protocol within the FTDI ICs. There is no need for a user to create drivers or software to encode or decode a USB message. It is all done for the designer, thus reducing development time and costs. For a complete listing of FTDI products please visit the website at: http://www.ftdichip.com/FTProducts.htm
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## Appendix A – Terminology

<table>
<thead>
<tr>
<th>Term</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CRC</td>
<td>Cyclic Redundancy Check</td>
</tr>
<tr>
<td>EOP</td>
<td>End of Packet</td>
</tr>
<tr>
<td>FTDI</td>
<td>Future Technology Devices International</td>
</tr>
<tr>
<td>PID</td>
<td>Product Identifier Field</td>
</tr>
<tr>
<td>SE0</td>
<td>Single ended Zero</td>
</tr>
<tr>
<td>USB</td>
<td>Universal Serial Bus</td>
</tr>
</tbody>
</table>
Appendix B – References

USB Implementers Forum – www.usb.org

Appendix C – Revision History

<table>
<thead>
<tr>
<th>Version</th>
<th>Draft/Release</th>
<th>Date</th>
</tr>
</thead>
<tbody>
<tr>
<td>Draft</td>
<td>First Draft</td>
<td>30/09/2009</td>
</tr>
<tr>
<td>1.0</td>
<td>First Release</td>
<td>11/11/2009</td>
</tr>
</tbody>
</table>